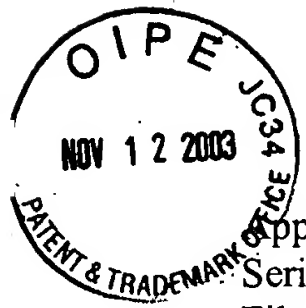


Image 2812



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hideomi Suzawa et al. Art Unit : 2812
Serial No. : 09/852,672 Examiner : Viktor Simkovic
Filed : May 11, 2001
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants submit the references listed on the attached form PTO-1449.

Applicants note that U.S. Application No. 09/852,282, which is the subject of U.S. Patent Application Publication US 2002/0006705 (Desig. ID AA), is directed to subject matter related to that of the present application.

This statement is being filed after a first Office action on the merits, but before receipt of a final Office action or a Notice of Allowance. A check for \$180 in payment of the late submission fee of §1.17(p) is enclosed. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: November 12, 2003


John F. Hayden
Reg. No. 37,640

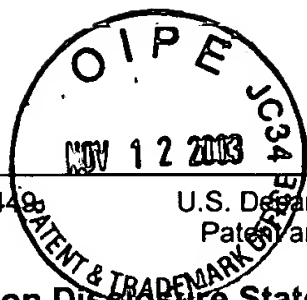
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Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 12732-038001	Application No. 09/852,672
	Applicant Hideomi Suzawa et al.		
	Filing Date May 11, 2001	Group Art Unit 2812	

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Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Sub-class	Filing Date If Appropriate
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Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AX	10-233511	09/02/1998	JAPAN			Full	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	